

Degradation of AlInAs/InGaAs/InP quantum cascade lasers due to electrode adhesion failure

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ABSTRACT

In this paper external degradation type of failure of Quantum Cascade Laser is analyzed. The failure mode discussed in the work is connected with the damage of the gold, top electrode. We show how fabrication faults translate to degradation of device and monitor temperature distributions as well as electrical characteristics of the device during the process.

The aim of this research is to demonstrate how external degradation process develops in case of AlInAs/InGaAs/InP quantum cascade lasers.

1. Introduction

Studies of degradation mechanisms of semiconductor lasers based on the GaAs family of materials have been performed from mid 1960s [1–3] beginning with those concerning GaAs homojunction laser diodes. After the development of heterostructure lasers and the achievement of CW operation at room temperature (early 1970s), the problem of degradation of semiconductor lasers became apparent. Reliability of many kinds of semiconductor devices has been greatly improving for about 50 years. Throughout this period, various degradation mechanisms have been discovered leading to increased understanding of processes occurring in devices and consequently improving reliability.

This paper focuses on the degradation mechanism present in quantum cascade lasers (QCLs)-type of semiconductor lasers based on intersubband transitions. QCLs were demonstrated for the first time in 1994 in Bell Labs by group of prof. F. Capasso [4]. Since then, QCLs underwent a rapid development. Despite significant improvement of parameters of devices [5–7] and optimization of their construction [8,9], reliability and device lifetime are still two critical parameters of the QCLs, deciding about further development and limiting their applications. Currently, the knowledge about processes responsible for degradation of quantum cascade lasers is still limited; there are not many papers published dealing with this problem from experimental point of view [10–12]. The common belief, although not fully experimentally confirmed, is that QCLs are more reliable devices compared to interband edge emitters. This is based on the fact that in case of intersubband semiconductor lasers there is no reabsorption of emitted

radiation at the laser facets, which is one of the main causes for interband edge-emitters failure [13,14]. The process of reabsorption leads to the local overheating of facets beyond the temperature of melting point of semiconductor material, which in turn results in catastrophic optical mirror damage (COMD), i.e., irreversible damage of device [15].

Thermal processes in QCL results from large electrical power density required for their operation and low thermal conductivity of complex, multilayer heterostructure. Those factors lead to strong local heating inside the device, resulting in significant overheating of active region [16]. High overall temperature of laser core causes a leakage of electrons from the upper laser level into delocalized continuum states and backfilling of the lower laser level by electrons from the subsequent injector. Both effects decrease the population inversion. These are the main limiting factors of high temperature operation of the devices.

Our previous papers [17–19] on degradation problems in QCLs concerned internal degradation mechanisms resulting from overheating of the active area, due to operation conditions (high current and high voltage). Internal degradation usually takes place in the active region and is accelerated by strain, temperature and injection current. However, degradation was observed in various batches of devices, that could not be regarded as internal, and seemed to appear randomly. Careful investigation of several batches of devices, revealed the pattern, that connected the failure of devices to fabrication steps, namely to the dielectric and gold deposition. The work has shown that both technological processes are critical for stable, reliable device and increasing the yield.

In this paper we present results of examination of external degradation mode of QCL. External degradation can occur in different

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Table 1

The layer sequence of lattice matched $\text{Al}_{0.48}\text{In}_{0.52}\text{As}/\text{In}_{0.53}\text{Ga}_{0.47}\text{As}/\text{InP}$ QCL structure.

		Thickness	Material	Doping
MOVPE	Upper waveguide	500 nm	InP	$n = 8 \times 10^{18} \text{ cm}^{-3}$
		1.5 μm	InP	$n = 1 \times 10^{17} \text{ cm}^{-3}$
		1.5 μm	InP	$n = 4 \times 10^{16} \text{ cm}^{-3}$
MBE		500 nm	InGaAs	$n = 4 \times 10^{16} \text{ cm}^{-3}$
	Active region	50 x	AlInAs/InGaAs	
	Lower waveguide	500 nm	InGaAs	$n = 4 \times 10^{16} \text{ cm}^{-3}$
	Substrate	500 μm	InP	$n = 2 \times 10^{17} \text{ cm}^{-3}$

parts of the device: mirror, electrodes, heatsink, bonding, metal coating of the heatsink. This work presents impact of electrode adhesion failure on electrical and thermal parameters and in the end on the degradation of mid-IR, InP-based QCL.

2. Investigated devices

The study of degradation mode was performed for lattice matched AlInAs/InGaAs/InP QCL grown by combined MBE and MOVPE. It was designed for emission at about 9.2 μm . Growth of the active region was performed by solid source MBE on Riber Compact 21T reactor [20]. MOVPE overgrowth was performed using Aixtron 3 × 2" FT LP-MOVPE system with vertical, shower-head reactor. The AlInAs/InGaAs active region of the laser was grown on lightly doped ($n = 2 \times 10^{17} \text{ cm}^{-3}$) InP substrate serving as a lower waveguide. The InP top waveguide consisted of 1.5 μm , $n = 4 \times 10^{16} \text{ cm}^{-3}$ layer followed by 1.5 μm , $n = 1 \times 10^{17} \text{ cm}^{-3}$ layer. The whole structure was completed by highly doped ($n = 8 \times 10^{18} \text{ cm}^{-3}$) contact layer. The details of layer structure of the laser are listed in the Table 1 [20].

The epitaxial wafers were processed in a standard double trench (DT) design with wet etching of trenches. Si_3N_4 was used to provide electrical isolation. For current injection, 15 μm wide window was opened in the insulator. Laser chips, 2 mm long, were soldered epi-side up to Au-plated AlN submount, and then to copper heatsinks using indium solder. Details of the processing technology are given in our earlier papers concerning InP-based QCLs [21]. In this paper uncoated devices were investigated.

3. Experimental results

To perform complex analysis of degradation of investigated AlInAs/InGaAs/InP QCLs, complementary experimental techniques were applied. To determine electrooptical parameters and their evolution during degradation process Light-Current-Voltage (LIV) curves were registered [22]. Analysis of thermal processes occurring at the laser facet: localization of the heat sources and defects on the facets, identification of heat propagation directions was achieved by performing thermoreflectance (TR) measurements [23,24]. To gain insight into microscopic failure mechanisms of QCLs, the degraded lasers were analyzed by means of focused ion beam (FIB) and scanning electron microscope (SEM) techniques with energy dispersive spectroscopy (EDS) analysis. Additionally, the devices were inspected by means of optical microscopy, which allow observation of external damage to device at the facets, solder, heatsink and electrodes.

First part of investigation included performing electrooptical characterization and visual inspection of the investigated devices. Fig. 1(a) shows comparison of LIV measurements for investigated laser chip before and after mounting to AlN and copper heatsink. For reference purposes, Fig. 1(a) contains LIV characteristic of mounted chip from the same batch, registered under the same experimental conditions (grey line). Compared to reference device, the investigated chip exhibits additional current path, what manifests in increased threshold current value.

In LIV data we observed increase of the threshold current of investigated device after mounting process. The value of threshold current increased by factor 2 from 1.26 A to 2.5 A while maintaining almost the same threshold voltage at the level ~ 11.5 V. This behaviour of the device suggests creating an additional path for the current flow. Optical inspection with microscope, presented in Fig. 1(b), revealed area at front facet, where the gold from the electrode seems to shorten the device by touching the facet at the position of AR layers, what is even better visible in SEM image presented in Fig. 2(c). Such features were frequently observed in batches of devices, in which we have also identified problems with either dielectric deposition or metal deposition, or usually both.

Investigation of thermal properties of the laser was performed by CCD-thermoreflectance spectroscopy (CCD-TR) [25]. Fig. 2(a–f) presents temperature distribution maps covering the region around active area on the front facet of investigated QCL, covering the region around active area. Maps were registered for the same experimental conditions: pulse width of 10 μs , frequency 20 kHz, heat sink temperature of 20 °C and for different driving currents in the range from 0.3 A to 1.5 A. Maps in the upper row are intentionally presented in different temperature range than maps in the lower row, to enhance the information about AR temperature.

For the first two maps (Fig. 2(a) and (b)) which were registered at 0.3 A and 0.5 A, maximal temperature increase is not localized in the active area but on the area of the gold overlapping on the epitaxial layers. Increase of temperature reaches 240 K for driving current of 0.5 A. Such results confirm, that gold overlapping on the epitaxial layers created additional current path and were the reason of observed overheating in this area and also caused increase of the threshold current for the laser (Fig. 1(a)). For larger driving current of 0.7 A, increase of temperature occurs only in the active area region. That suggests that additional current path was interrupted, most probably the burn-out took place, resulting in formation of isolation layer beneath gold. It is worth noticing, that in map Fig. 2(c), the shape of the feature (gold overlapping on facet) has changed, covering the area of AR of the device. Maps registered for driving currents of 1.1 A, 1.3 A and 1.5 A (Fig. 2(d)–(f)), present temperature distribution shape and temperature increase in the active area, which typical for this type of InP-based QCLs (compare [26]).

The vertical line scans for selected maps are presented in Fig. 3(a) and maximal temperature increases in AR extracted from all registered maps are presented in Fig. 3(b).

Black and blue lines in Fig. 3(a) presents temperature data for line scans taken at the point A - center of the active area. The green and red lines present line scans taken at the point B - point located on the facet in the vicinity of the overlapping gold. For area of gold overlapping on the laser facet, high temperature increases are registered: for 0.3 A–120 K, for 0.5 A–246 K. These are much higher values than those registered for the active area, and are the result of high current density which flows in this spot. The size of the tip of gold overlap was estimated to be of about 6 μm . Additionally, such parasitic current path has high resistance due to low quality gold to heterostructure electrical contact.

The maximal temperature increases (ΔT) presented in the Fig. 3(b), show that temperature rises very fast in the area of defect and goes to zero abruptly for the next current step. The temperature rise in the active area reaches ~ 90 K for current of 1.5 A. The LIV curve registered after the TR measurement at 0.7 A shows that the threshold current decreased to the value registered before mounting, i.e. to 1.25 A (threshold voltage remained unchanged), which confirms that parasitic electrical path has been destroyed by burn-out.

Further thermal measurements were carried out for higher thermal load, resulting from driving the device with longer current pulses of 20 μs . Fig. 4(a–c) presents temperature distribution maps on front facet of investigated QCL, registered for the same experimental conditions: pulse width of 20 μs , frequency 20 kHz, heat sink temperature of 20 °C

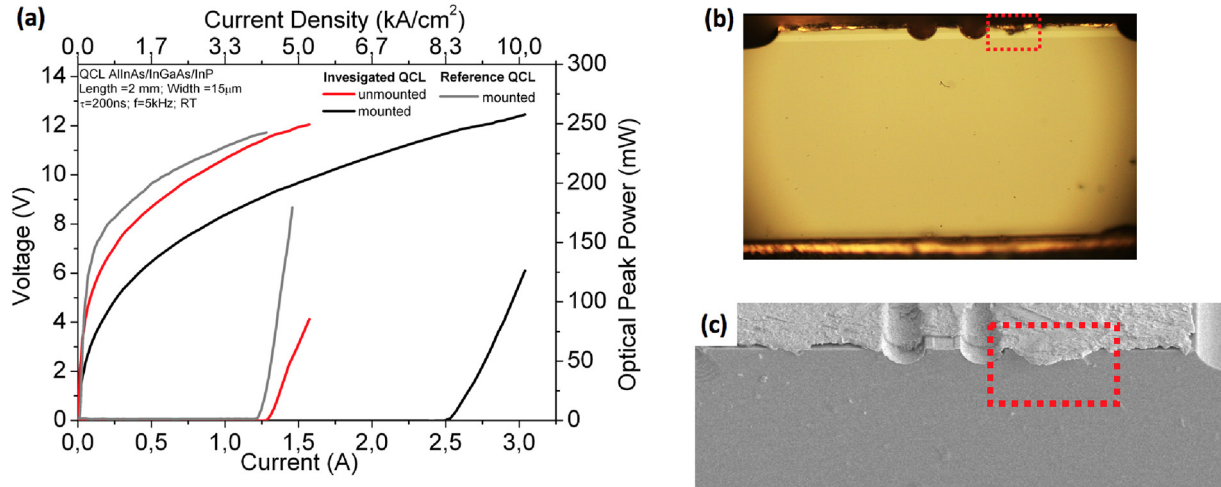


Fig. 1. (a) LIV characteristics for investigated and reference QCLs. Red line corresponds to data for QCL laser chip before mounting, black line - after mounting. Grey lines were registered for reference device from the same batch. The data is collected in pulse mode using 200 ns current pulses and 5 kHz repetition rate at room temperature (RT). (b) Optical microscopy image and (c) SEM image showing front facet of the laser with an area where gold overlaps on epitaxial layers. (For interpretation of the references to color in this figure legend, the reader is referred to the web version of this article.)

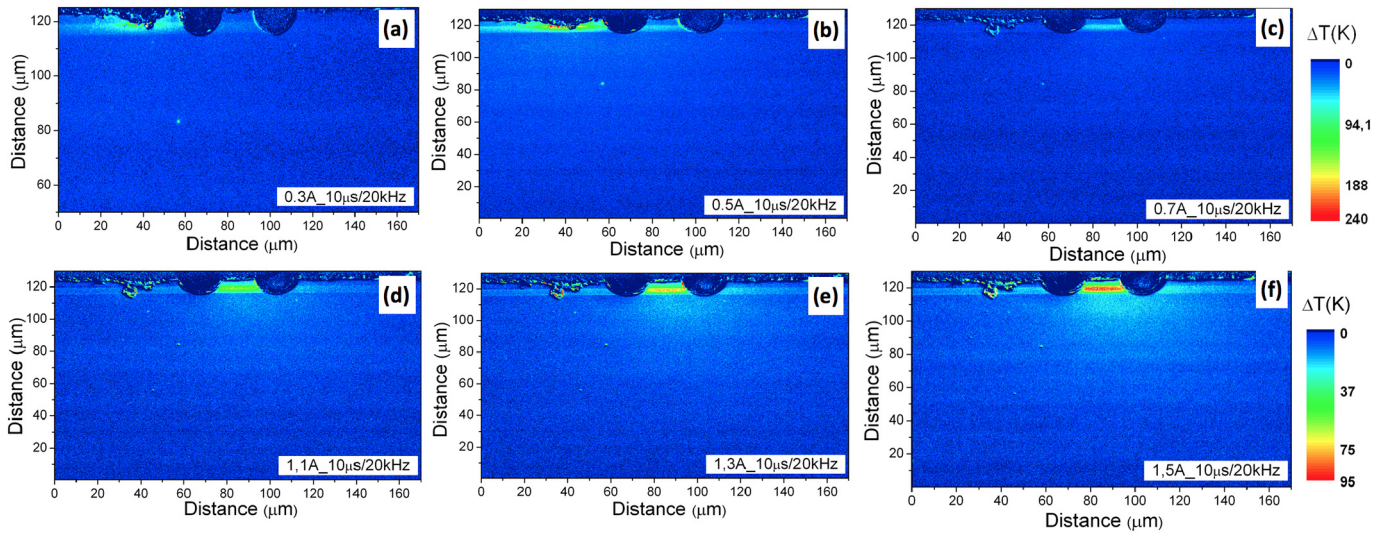


Fig. 2. Temperature distribution maps on the front facet of AlInAs/InGaAs/InP QCLs, measured for pulse width 10 μ s and frequency 20 kHz for different driving currents change in the range 0.3 A–1.5 A. Maps are intentionally presented in different temperature ranges to enhance the features.

and different driving currents in the range from 0.3 A to 1.5 A. Fig. 4(d) presents the maximal temperature increases (ΔT) and Fig. 4(e) vertical temperature scans taken at the center of the active area.

Even for low driving current (0.3 A), non-uniform temperature distribution in the active area of the QCL can be observed, with spots exhibiting higher temperature. With the increase of current up to 1.5 A these features are becoming more pronounced. For current of 1.6 A, the laser stopped working – electrical resistance of the device dropped to 2 Ω , no heating is observed (Fig. 4(d)).

Visual inspection performed by optical microscope (Fig. 5) shows damaged areas at different parts of the device. Top view of device presented in Fig. 5(a), shows the electroplated gold layer. Destruction of the metal layers is clearly visible, reaching deep into the semiconductor structure. Fig. 5(b) presents the image of the front facet of the device. The part of mirror with AR and overlapping gold is zoomed into in the inset. In the AR, overheating patterns are clearly visible, indicating high local temperature rise, leading to local melting taking place in the region where the metal migrated on the front facet of the laser. The mirror was clear before the measurements were taken, clearly indicating that features were formed during the degradation of the

device.

To investigate in more details the failure of the QCL, degraded laser was analyzed by means of SEM imaging, FIB milling and also by EDS analysis.

Fig. 6(a) presents SEM image of the degraded device. Several FIB cuts were performed perpendicular to the layers at multiple locations along the laser ridge near of the area, starting at the region where large destruction of metal layers was visible in the top view of device. The dashed lines indicate places, where FIB cuts were performed. Fig. 6(b–e) present SEM images for successive FIB cuts. All images of planes revealed by the cuts in the region of interest show problem with gold adhesion, as well as severe melting taking place in the AR layers and top waveguiding layer of the semiconductor structure (Fig. 6(c–e)).

Images shown in Fig. 6, present damage and voids in the area of the interface between semiconductor QCL laser chip and gold layer. It can be observed, that problem with adherence of gold layer consequently leads to damage occurring in the active region of the laser. FIB cuts were performed further along the ridge, away from the damaged area, showing no further damages in active region.

EDS analysis confirms that droplets visible in the active are consist

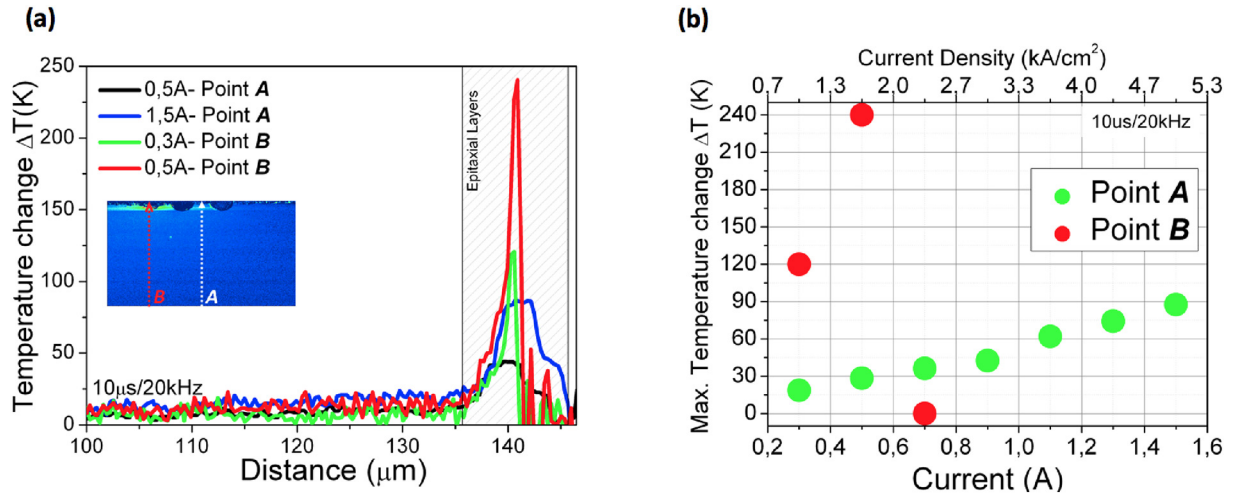


Fig. 3. (a) Temperature distribution line scans across the laser facet, taken at the center of the active area (Point A) and at the area of gold overhanging (Point B) perpendicular to the epitaxial layers. Inset presents a TR map with marked cross-section locations. (b) Maximum temperature increase as a function of driving current. (For interpretation of the references to color in this figure legend, the reader is referred to the web version of this article.)

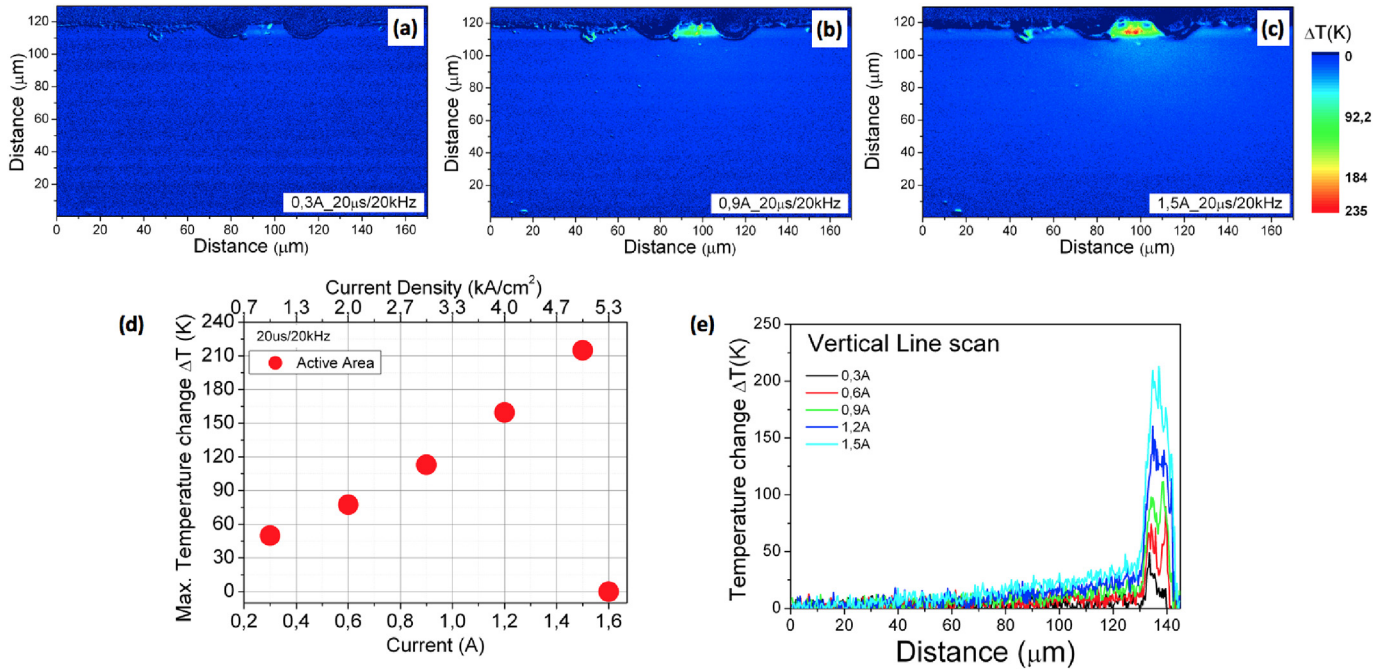


Fig. 4. (a–c) Temperature distribution maps on the front facet of AlInAs/InGaAs/InP QCLs, measured for pulse width 10 μs and frequency 20 kHz for different driving currents change in the range 0.3 A–1.5 A. (d) Maximum temperature increase as a function of driving current. (e) Temperature distribution line scans across the laser facet, taken at the center of the active area perpendicular to the epitaxial layers.

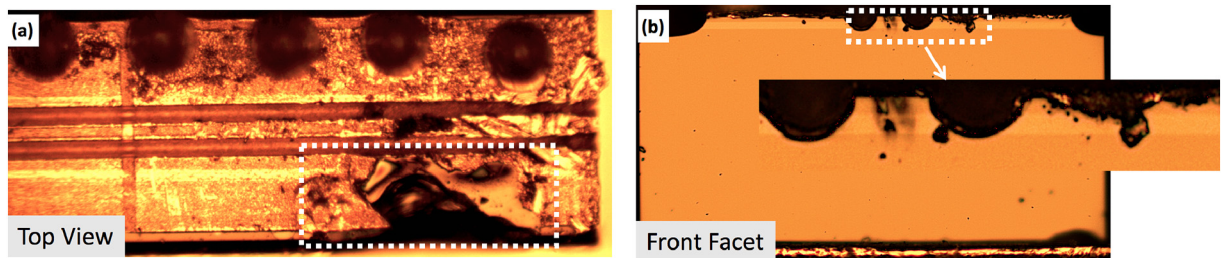


Fig. 5. Optical microscopy images showing top view of the laser chip(a) and front facet of the laser. The frames show areas that have been degraded.

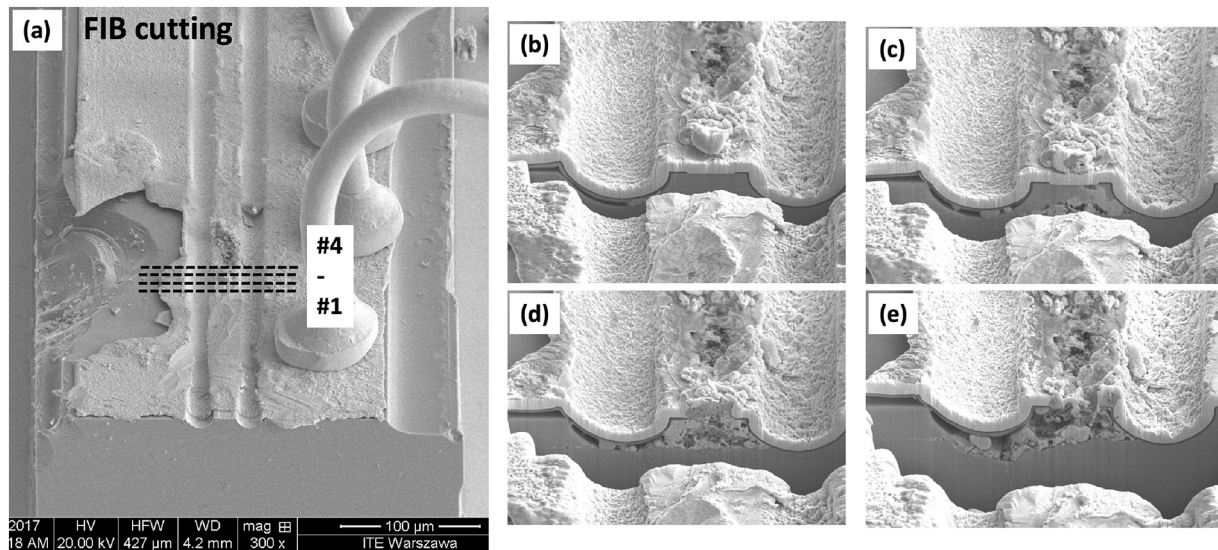


Fig. 6. SEM images of part of the top of the diode laser under test after failure. (a) Locations of cross-sections done by FIB, (b)–(e) SEM images of cross sections.

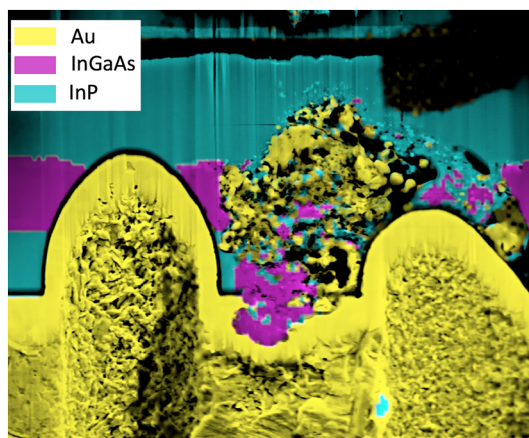


Fig. 7. EDS analysis of part of the active area of the QCL after cross-sections done by FIB.

of gold from upper metal gold layer (Fig. 7). Such situation is known to take place in semiconductor lasers, as metals from degraded electrodes or solder electromigrate inside the chip. This phenomenon is enhanced by current injection (electric field) and temperature.

However, it should be emphasized that the reason for the explosion and melting of active area resulting from overheating is not started in the internal part of the device as a result of low heat dissipation, but rather stems from the external factor - bad adhesion of gold to the semiconductor. This problem of poor adhesion in the initial phase of electrooptic and thermal tests, led to an increase in the threshold current caused by the creation of an additional channel for current flow. During thermal measurements with increased thermal load, the problem of bad gold contact became apparent. The increase local temperature caused by the lack of good thermal contact, led to destruction of metal layers on the upper laser surface, consequently leading to the damage of the whole device.

4. Conclusions

In this paper, we have presented and analyzed external degradation type of failure of the QCLs, associated with the damage of the top electrode. It is shown, how fabrication faults, identified in selected batches of devices, translate into degradation of device. We have registered temperature distributions and electrical characteristics of the

device during the degradation process. Results show that the damage visible in AR does not necessarily stem from the fact of overheating resulting from high current flowing through active region during operation. We have observed that poor adhesion of dielectric away from the active regions itself, can lead to extensive thermal degradation of devices. The processes of electromigration of metals (i.e. gold) of top electrode are strongly enhanced by increased temperature (overheating) and high gradient of electric field.

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